

amplifier - LOCK

TABLE 1 SQUELCH CONTROL FULLY COUNTERCLOCKWISE, NO INPUT SIGNAL			
	E	B	C
Q5	.4	1.7	.55
Q6	.6	1.2	8.0
Q7	.6	1.3	8.0
Q8	0	.7	.1
Q9	0	.5	3.6
Q10	0	.7	.1
Q11	—	6.0	—

TABLE 2 SQUELCH CONTROL FULLY COUNTERCLOCKWISE, NO INPUT SIGNAL			
	E	B	C
Q5	.5	0	7.0
Q6	.6	.8	.8
Q7	.6	.3	5.0
Q8	0	.3	12.5
Q9	0	.7	.1
Q10	0	.1	1.4
Q14	12.5	12.5	11.8

NOTES:

1. COMPONENT NUMBERS ARE IN THE FOLLOWING GROUPS:

1-199 PARTS ON THE MAIN CIRCUIT BOARD
201-299 PARTS IN THE CASE
301-399 PARTS IN THE BATTERY CHARGER
401-499 PARTS ON THE RF DETECTOR

2. ALL RESISTORS ARE 1/4 WATT, 5% TOLERANCE, UNLESS OTHERWISE NOTED. RESISTOR VALUES ARE IN OHMS, K-1000, M-1,000,000.

3. CAPACITORS EQUAL TO OR LESS THAN .1 ARE IN PF (PICOFARADS); ALL OTHER CAPACITORS ARE IN UF (MICROFARADS) UNLESS OTHERWISE MARKED.

4. INDUCTORS ARE SHOWN IN MH (MILLIHENRIES) AND μ H (MICROHENRIES).

5. \bigcirc THIS SYMBOL INDICATES A DC VOLTAGE MEASUREMENT TAKEN WITH A HIGH INPUT IMPEDANCE VOLTMETER FROM THE POINT INDICATED TO CHASSIS GROUND UNDER THE FOLLOWING CONDITIONS:

A. NO INPUT SIGNAL.
B. SQUELCH CONTROL FULLY COUNTERCLOCKWISE.
C. VOLUME CONTROL FULLY COUNTERCLOCKWISE.
D. LOWEST FREQUENCY OSCILLATOR CRYSTAL SET POINT.
E. TRANSMITTER VOLTAGES - KEPT WITHIN 5% TOLERANCE.

6. \odot THIS SYMBOL INDICATES CHASSIS GROUND.
 \bigcirc THIS SYMBOL INDICATES A SOLDERED CONNECTION TO THE MAIN CIRCUIT BOARD.

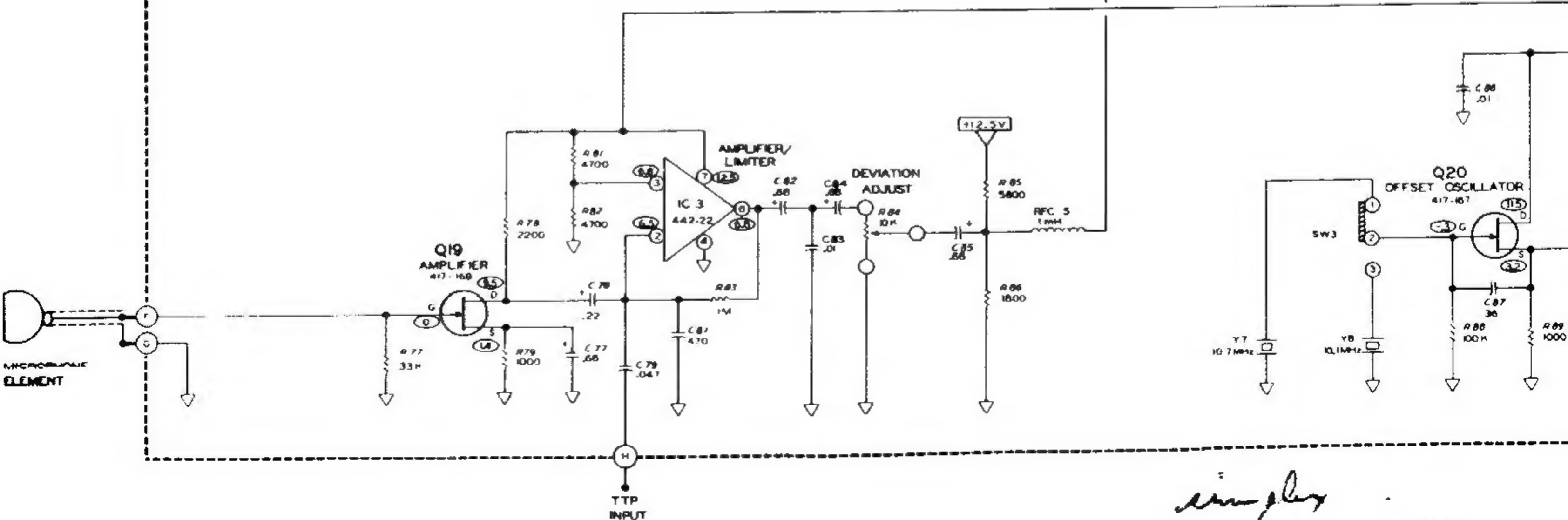
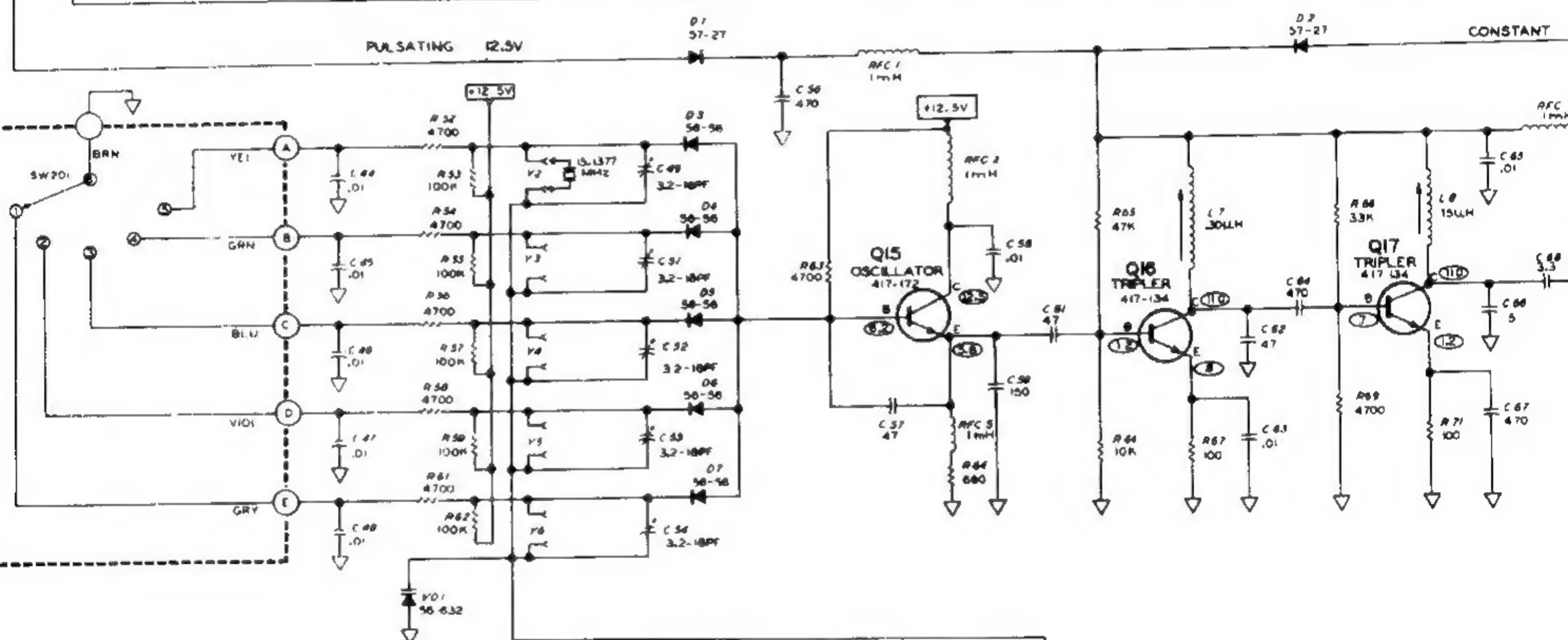
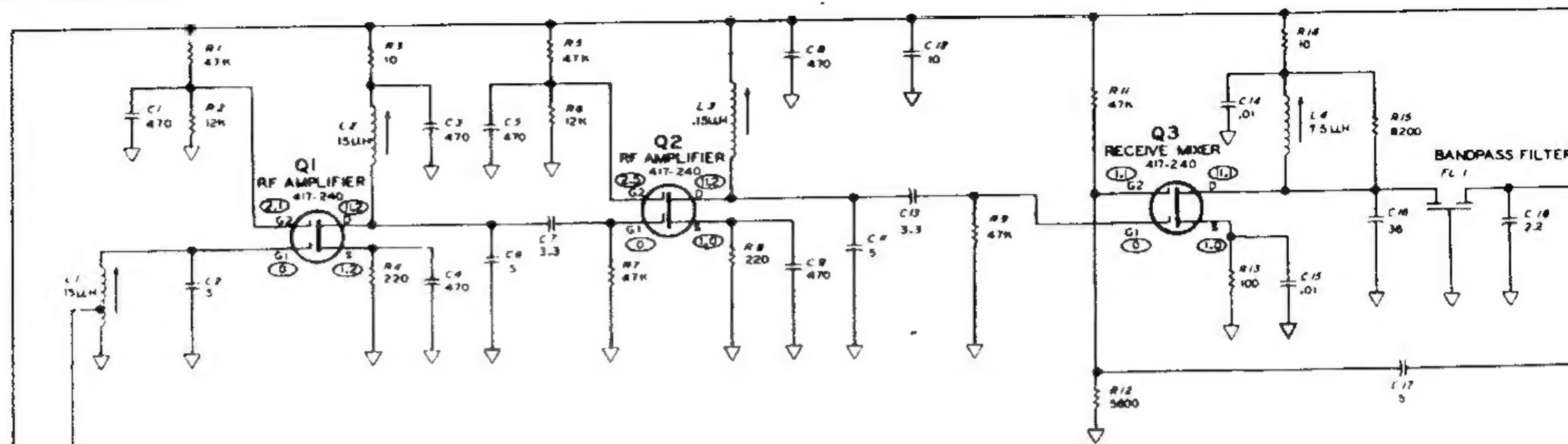
\blacklozenge SEE TABLES 1 AND 2 FOR VOLTAGES.
 \blacktriangledown THIS SYMBOL DENOTES A SHOCK ADJUST TO THE POINT BUILDER.

7. REFER TO THE "CIRCUIT BOARD LAYOUTS" FOR THE PHYSICAL LOCATION OF PARTS.

8. **TP** INDICATES TEST POINT.

9. * INDICATES TEST POINT USED ONLY WHEN ADJUSTMENTS PERFORMED WITHOUT INSTRUMENTS.

MAIN CIRCUIT BOARD



simply
-LOOK.

